

**Meeting:** Board of Studies – Electronics and Communication Engineering

**Date and Time:** 07<sup>th</sup> July 2019 at 10:00 AM

**Venue:** AITS, RAJAMPET.

Minutes of 11<sup>th</sup> Board of Studies- Electronics and Communication Engineering meeting held on 07<sup>th</sup> July 2019 at AITS, Rajampet.

**Members Present:**

1. Prof. CH.Nagaraju, HOD-ECE & Chairman, BOS
2. Prof. B. Abdul Rahim, Professor-ECE & Member
3. Dr.S.Fahimuddin, Associate Professor-ECE & Member
4. Sri. J.Chinna Babu, Assistant Professor-ECE & Member
5. Dr.K.Ramalinga Reddy, HOD & Professor, GNITS, Hyderabad & Outside Subject Expert
6. Dr. M. Ramasubba Reddy, Professor, IIT Madras, Chennai & Outside Subject Expert
7. Sri.Y.Avinash, Mirafra Technologies, Hyderabad & Industry Expert
8. Sri. B .Abdul Saleem, TCS, Hyderabad & Meritorius Alumni member

**Members Absent:**

1. Dr. D. Srinivasa Rao, JNTUH, Hyderabad & University Nominee

**RESOLUTIONS**

**Item No. 1 : To prepare Syllabi for I & II B.Tech and PG Courses as per 'R19' regulations**

Dr.Ch.Nagaraju, Chairman-BOS initiated the meeting and requested the members to give suggestions regarding I & II B.Tech and PG Courses for 'R19' regulations as per the guidelines given by the AICTE & APSCHE.

**• B.Tech (UG)**

- 1) It is here by resolved to combine the topics related to analog circuits from the courses Electronic Circuits (II-I), Pulse and Digital Circuits (II-II) and Analog & Digital Integrated Circuits (III-I) of R17 regulations into two courses namely Electronic circuits in II year I semester and Analog IC Applications in II year II semester. Simultaneously, include the concepts related to digital ICs in VLSI Design in III year II semester as suggested by Dr. M. Rama Subba Reddy and Dr. K. Ramalinga Reddy.

- 2) In continuation to the above resolution, the members have come to a conclusion that, the laboratory courses shall reflect the respective theory courses.
- 3) The experts have proposed to bring all the Professional Elective courses into one group such that the student will opt for Six Professional Electives based on the pre-requisites and whenever the courses are offered.
- 4) It is hereby resolved that, the list of Open Electives for ECE students should be the Electives offered by all other departments as suggested by Dr. M. Ramasubba Reddy and Mr. Y.Avinash.
- 5) The experts have proposed that students should undergo 3 to 4 weeks of Internship during the UG program and this Internship will have the weightage of 2 credits. These credits should reflect in IV year II semester.

• **M.Tech-Embedded Systems (PG)**

- 1) It is hereby resolved to include Research Methodology & IPR course and Audit Course in I semester with 1 credit each. And also Mini Project with 2 credits is introduced in II semester.
- 2) It is here by resolved to include two laboratory courses in I & II semesters as per AICTE model curriculum.
- 3) It is hereby resolved to introduce Open electives that are to be offered by other departments and Dissertation Phase-I (with 10 credits) in III semester. Dissertation Phase-II (with 16 credits) is included in IV semester.

**Item No. 2 : To suggest the panel of examiners list to the academic council for approval.**

- 1) It is resolved to include PhD holders from the reputed institutions as examiners and paper setters.

**Item No. 3 : Any other item with the permission of the Chair**

- 1) Comments and Suggestions have been taken from the experts regarding the preparation of R-19 Curriculum for I & II B.Tech. and PG Courses based on AICTE and APSCH model curriculum.

The Chairman of BOS has delivered vote of thanks.



  
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