

**ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCES: RAJAMPET
(AUTONOMOUS)**

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Value added Course on Advanced VLSI Concepts (In coordination with ANTRIX ECE)

Convener: Dr. CH. Nagaraju, HOD, Dept.of ECE

Co-ordinator: Mr. G. Sudha Kiran

**Organizing Committee: Mr. G. Obulesu
Miss. G. V. Padmaja
Mrs. B. Prasanthi**

**Resource Persons: 1) Dr. B. Abdul Rahim
2) Dr. S. Fahimuddin
3)Mr. N.Bala Dastagiri
4)Mr. G. Sudha Kiran
5) Mr. S.Nazeer Hussain
And
Resource person(s) from Industry**

Target Students: III Year ECE students

**Date and Time: 26/02/2018 to 09/03/2018 from 5:00PM onwards for a
Minimum duration of one hour.**

**Registration: Interested candidates can register for the workshop through
thelink:<https://goo.gl/forms/fwssDOJBBMndrPAh2>.
However attendance and assignment are mandatory for the
issue of workshop certificate.
Last date for the registration is 23/02/2018.**

Course Structure:

Course	Class	No. of students	Duration
1.Impact of VLSI on Modern Technology 2. VLSI Back end and front end design flows Importance of VLSI -EDA tools 3. Importance of Optimization Tools in VLSI Design 4. Designing of basic Digital Circuits using GDK components and their programming through VHDL 5. Importance of VLSI in Medical electronics 6.Lab sessions on Tanner tools and Xilinx tools	III ECE	85	6 hrs/week (Maximum of 12 hours)

Prerequisite:

This course has no specific prerequisites. However some familiarities with the following are especially helpful.

- VHDL Programming
- HDL Simulation using EDA tools
- Digital Design Concepts

Course Objectives:

The goal of this course is to familiarize students with the concepts and practical skills required to successfully program and Verify digital systems using HDLs. After finishing the course, students should feel comfortable in building their own digital systems and verifying those.

Course outcome:

After completion of this course, the student can able to implement different digital design circuits using GDK files and EDA tools.

Assessment:

1. At the end, each student has to complete the assignment on the topic covered in this course.